AUS920010895US1 1/8

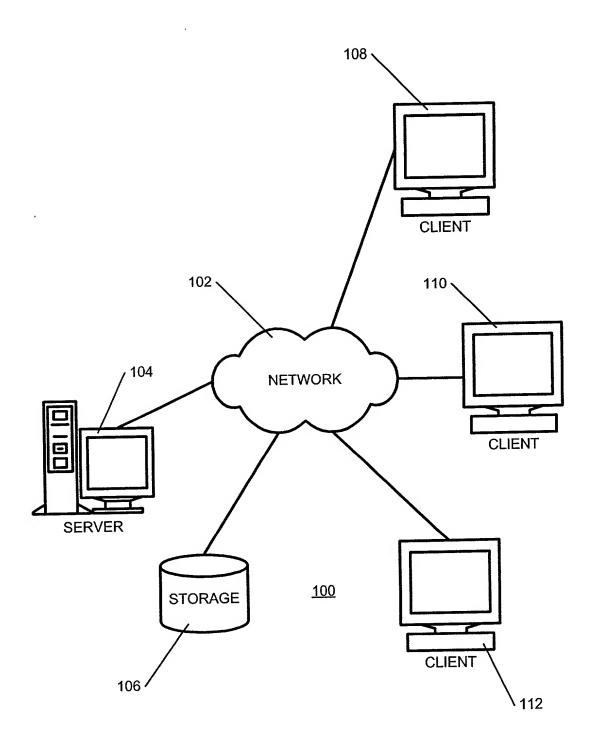
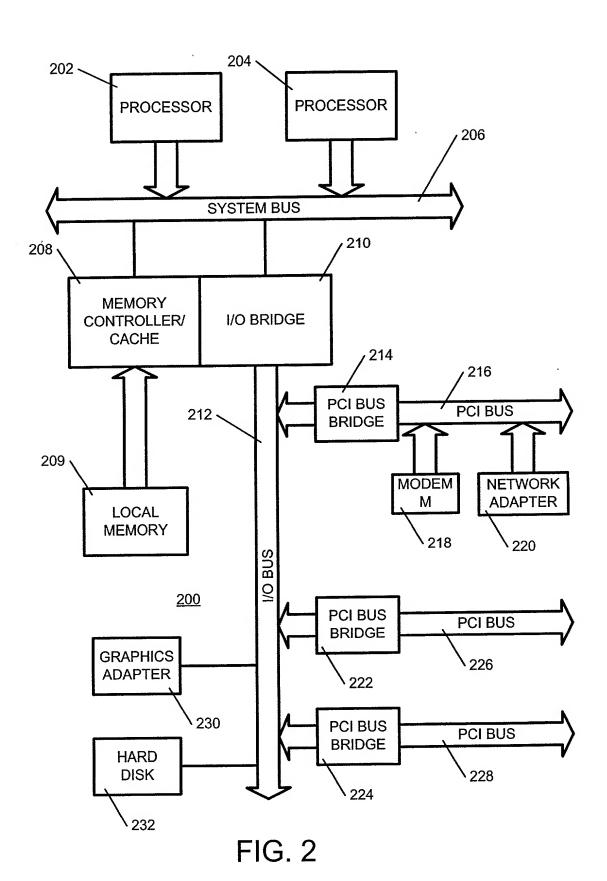
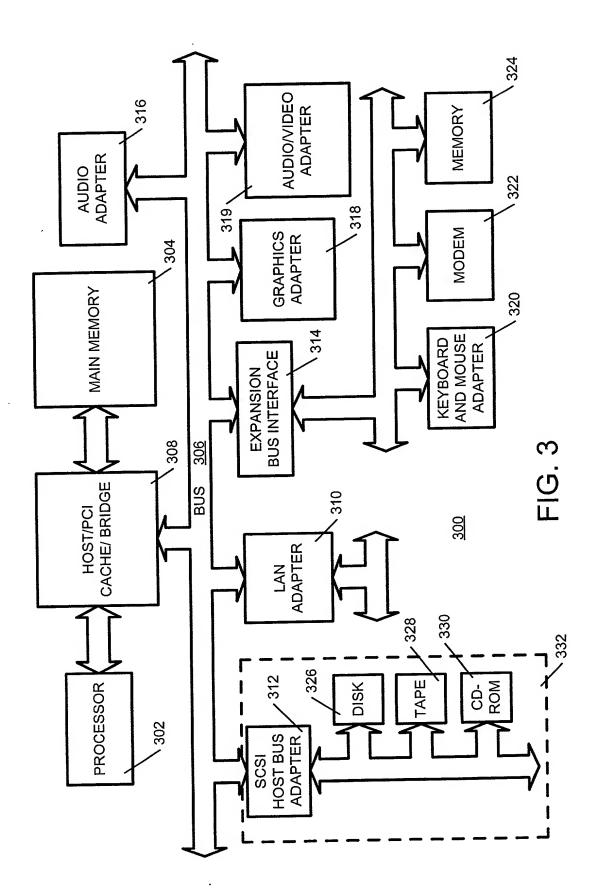


FIG. 1







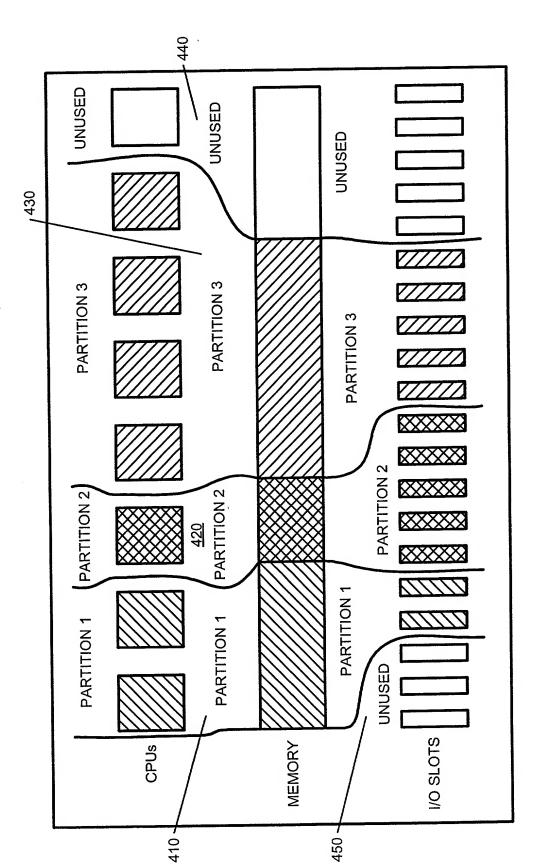


FIG. 4

PARTITION1 CPU1 & CPU2 M1 - M50 SLOT & SLO PARTITION2 CPU3 M51 - M75 SLOT & SLO PARTITION3 CPU4 - CPU5 M76 - M150 SLOT 1 & SLO		CPU'S	MEMORY LOCATIONS	I/O SLOTS
CPU ₃ M ₅₁ - M ₇₅ CPU ₄ - CPU ₅ M ₇₆ - M ₁₅₀	PARTITION ₁	\mathtt{CPU}_1 & \mathtt{CPU}_2	M ₁ - M ₅₀	SLOT ₄ & SLOT ₅
ARTITION ₃ CPU ₄ - CPU ₅ M ₇₆ - M ₁₅₀	PARTITION ₂	CPU3	M ₅₁ - M ₇₅	SLOT ₆ & SLOT ₁₀
	PARTITION3	CPU₄ - CPU₅	M ₇₆ - M ₁₅₀	SLOT ₁₁ & SLOT ₁₅

FIG. 5

I/O SLOTS	SLOT ₄ & SLOT ₅	SLOT ₆ - SLOT ₁₀	SLOT ₁₁ - SLOT ₁₅	
MEMORY LOCATIONS	M ₂₁ - M ₅₀	M ₁ - M ₂₀ & M ₅₁ - M ₇₅	M ₇₆ - M ₁₅₀	
CPU'S	CPU ₁ & CPU ₂	cPU₃	CPU₄ - CPU₁	
	PARTITION ₁	PARTITION ₂	PARTITION ₃	

FIG. 6

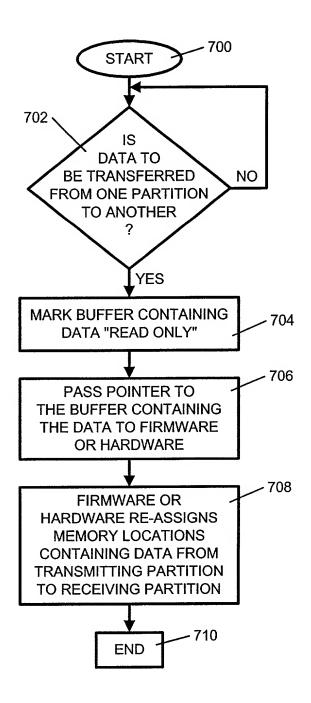


FIG. 7

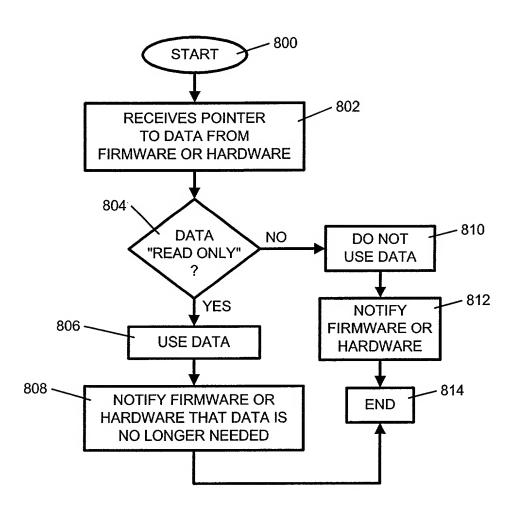


FIG. 8